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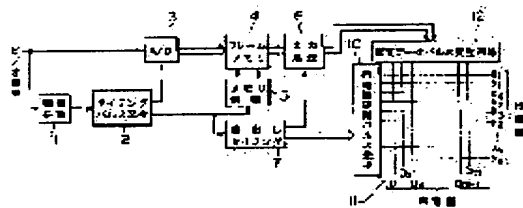
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(54) DRIVING METHOD FOR MATRIX SYSTEM PLASMA DISPLAY PANEL

(57)Abstract:

PURPOSE: To provide an exact display image corresponding to pixel data to be supplied by stopping discharge emission caused by maintenance discharge by generating discharge between paired row electrodes and column electrodes by impressing a maintenance erasure pulse to one of paired row electrodes and impressing an address to the column electrodes at same time.

CONSTITUTION: A row electrode driving pulse generating circuit 10 simultaneously impresses a maintenance erasure pulse EP of a negative voltage to respective row electrodes X1-Xn. A pixel data pulse generating circuit 12 generates an address pulse AP of a positive voltage having a polarity opposite to that of the maintenance erasure pulse EP and impresses this address to respective column electrodes D1-Dm at the same timing as the impression timing of the maintenance erasure pulse EP. Since such a maintenance erasure pulse of the negative voltage is impressed to row electrodes Y and the address pulse AP of the positive voltage having the polarity opposite to that of this maintenance erasure pulse EP is simultaneously impressed to column electrodes D, discharge is generated between the row electrodes X and Y and the column electrodes D. Therefore, most of electric wall charges of picture element cells are extinguished.



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